

PATENT APPLICATION  
IN THE U.S. PATENT AND TRADEMARK OFFICE  
January 9, 2001

Applicants: Masayuki KOBAYASHI et al  
For: IMAGE QUALITY CORRECTING CIRCUIT  
Serial No.: 09/700 495 Group: Unknown  
Filed: November 14, 2000 Examiner: Unknown  
International Application No.: PCT/JP00/01872  
International Filing Date: March 27, 2000  
Atty. Docket No.: Furusawa Case 53

Assistant Commissioner for Patents  
Washington, DC 20231

**AMENDMENT BEFORE FIRST OFFICE ACTION**

Sir:

Prior to issuance of the first Office Action in the  
above-identified application, kindly enter the following:

IN THE CLAIMS

Please amend Claims 5 and 23 as follows.

Claim 5, line 6; change "set pints" to  
---set points---

23. (Amended) The image quality correcting circuit  
defined in Claim 22, wherein the control range comparator 43  
comprises the upper limit comparator 41b for comparing the  
counted value Pn of the correcting characteristic point with  
the quadratically variable upper limit value YHn, and the  
lower limit comparator 42b for comparing the counted value Pn  
of the correcting characteristic point with the lower limit  
value YLn, while the control range setter 46 comprises the  
upper limit setter 44b for setting the upper limit value YHn  
and the lower limit setter 45b for setting the lower limit  
YLn.

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**FIRST CLASS MAILING CERTIFICATE**

Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service under 37 CFR 1.8 as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington DC 20231, on January 9, 2001.

  
Terrance F. Chapman

TFC/smd

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Correspondence: Amendment Before First Office Action  
dated January 9, 2001  
including enclosures listed thereon

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*Tm*